JUN 04 2007

Docket No.: 324758001US2
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Alverson et al.

Application No.: 10/663,895

Confirmation No.: 4522

Filed: September 16, 2003

Art Unit: 2109

For: DEFERRED TASK SWAPPING IN A
       MULTITHREADED ENVIRONMENT

Examiner: M. P. Wilser

INFORMATION DISCLOSURE STATEMENT TO RE-SUBMIT PREVIOUSLY CITED DOCUMENTS

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

Copies of the references on the Form PTO-1449 are provided.

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information that may be material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the cited information is, or is considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

Applicant believes that no fee is due since these documents were previously cited in an Information Disclosure Statement filed January 12, 2004. However, the Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0665, under Order No. 324758001US1.

Dated: 5/31/2007

Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT Form PTO-1449 (Modified) (Use several sheets if necessary)	COMPLETE IF KNOWN	
	Application Number	10/663,895
	Confirmation Number	4522
	Filing Date	September 16, 2003
	First Named Inventor	Gail A. Alverson
	Group Art Unit	2109
	Examiner Name	Michael P. Wilser
Sheet 1 of 2	Attorney Docket No.	324758001US2

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			

FOREIGN PATENT DOCUMENTS

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		Office	NUMBER	Kind Code (if known)				
		DE	DE19710252		Fujitsu Ltd. (English Abstract)	2/26/98		

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
		"Method of Tracing Events in Multi-Threaded OS/2 Applications," IBM Tech. Disclosure Bulletin, September 1993, pp. 19-22.	
		Agrawal, Hiralal, "Dominators, Super Blocks and Program Coverage," 21st ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, Portland, Oregon, January 17-21, 1994.	
		Alverson, Gail et al., "Tera Hardware-Software Cooperation," Proceedings of Supercomputing 1997, San Jose, California, November 1997.	
		Briggs, Preston et al., "Coloring Heuristics for Register Allocation," Department of Computer Science, Rice University, Houston, Texas, June 1989.	
		Briggs, Preston et al., "Coloring Register Pairs," ACM Letters on Programming Languages and Systems, Vol. 1, No. 1, March 1992.	
		Callahan, David et al., "Register Allocation via Hierarchical Graph Coloring," Proceedings of the ACM SIGPLAN '91 Conference on Programming Language Design and Implementation, Toronto, Canada, June 26-28, 1991.	
		Chow, Fred C. et al., "The Priority-Based Coloring Approach to Register Allocation," ACM Transactions on Programming Languages and Systems, Vol. 12, No. 4, October 1990, pp. 501-536.	
		Cook, Jonathan et al., "Event Based Detection of Concurrency," SIGSOFT '98 ACM, 1998, pp. 34-45.	
		Davidson, Jack W. et al., "Reducing the Cost of Branches by Using Registers," Proceedings of the 17th Annual Symposium on Computer Architecture," Seattle, Washington, May 28-31, 1990.	

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance <u>and not</u> considered. Include copy of this form with next communication to application(s).

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		OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
		Kolte, Priyadarshan et al., "Load/Store Range Analysis for Global Register Allocation," ACM-SIGPLAN, June 1993.	
		Knoop, Jens et al., "The Power of Assignment Motion," ACM SIGPLAN '95 Conference on Programming Language Design and Implementation," La Jolla, California, June 18-21, 1995.	
		Lal, George et al., "Iterated Register Coalescing," ACM Transactions on Programming Languages and Systems, Vol. 18, No. 3, May 1996, pp. 300-324.	
		Lang, Tomas et al., "Reduced Register Saving/Restoring in Single-Window Register Files," Computer Architecture News, Vol. 14, No. 3, June 1986.	
		Minwen, Ji et al., "Performance Measurements for Multithreaded Programs," SIGMETRICS '98, ACM, 1998, pp. 168-170.	
		Shim, SangMin et al., "Split-Path Enhanced Pipeline Scheduling for Loops with Control Flows, IEEE, December 2, 1998.	
		Tsai, Jenn-Yuan et al., "Performance Study of a Concurrent Multithreaded Processor," IEEE, 1998, pp. 24-35.	

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